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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/655,537	09/05/2003	Martin Goss	59643.00315	5578
32294	7590	11/17/2004	EXAMINER	
SQUIRE, SANDERS & DEMPSEY L.L.P.			CHOE, HENRY	
14TH FLOOR			ART UNIT	PAPER NUMBER
8000 TOWERS CRESCENT			2817	
TYSONS CORNER, VA 22182				

DATE MAILED: 11/17/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	10/655,537	GOSS, MARTIN
Examiner	Art Unit	
Henry K Choe	2817	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

1)  Responsive to communication(s) filed on 05 September 2003.

2a)  This action is **FINAL**.                            2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

4)  Claim(s) 1-23 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5)  Claim(s) \_\_\_\_\_ is/are allowed.

6)  Claim(s) 1-23 is/are rejected.

7)  Claim(s) \_\_\_\_\_ is/are objected to.

8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All    b)  Some \* c)  None of:  
1.  Certified copies of the priority documents have been received.  
2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 9/5/2003.

4)  Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_.  
5)  Notice of Informal Patent Application (PTO-152)  
6)  Other: \_\_\_\_.

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-23 are rejected under 35 U.S.C. 103(a) as being unpatentable over Samay et al (Fig. 1).

Regarding claims 1-4, 21 and 23, Samay et al (Fig. 1) discloses an amplifier circuit comprising input means (a terminal which receives RF INPUT signal) which receives signals (RF INPUT), a power transistor (Q1) which amplifies received signals (RF INPUT), and a first circuitry (L7, C7, L5, C5) which is connected at one end (bottom terminal of L7) to the power transistor (Q1) and at another end (upper terminal of L7) to a relatively low frequency shorting circuitry (C8) and wherein the first circuitry (L7, C7, L5, C5) being such that the another end (upper terminal of L7) is an open circuit to the different frequencies (DC signals). As described above, Samay et al (Fig. 1) discloses all the limitations in the claims except for that the first circuitry (L7, C7, L5, C5) having a length which is substantially less than a quarter wavelength of the different frequencies and a length in the range of .10 to .25 degrees of a signal envelope frequency and a length in the range of .15 to .20 degrees of a signal envelope frequency and a length in the range of .17 degrees of a signal envelope frequency. It would have been obvious to one of ordinary skill in the art at the time the invention was made to have implemented the specific values of the components, since they are based on the routine experimentation to obtain the optimum operating parameters.

Regarding claim 5, the first circuitry (L7, C7, L5, C5) is functionally equivalent to the claimed band stop filter.

Regarding claim 6, the first circuitry (L7, C7, L5, C5) is a resonant circuit.

Regarding claim 7, the resonant circuit (L7, C7, L5, C5) includes an inductor (L7) and a capacitor (C7).

Regarding claim 8, the inductor (L7) and the capacitor (C7) are arranged in series.

Regarding claim 9, the first circuitry (L7, C7, L5, C5) includes a first resonant circuit (L5, C5) having an inductor (L5) and capacitor (C5) in parallel and a second resonant circuit (L7, C7) having a capacitor (C7) and inductor (L7) in series.

Regarding claim 10, the first circuitry (L7, C7, L5, C5) provides a voltage feed path (from a drain of Q1 to L7 to +V).

Regarding claim 11, the first circuitry (L7, C7, L5, C5) has a relatively low impedance.

Regarding claim 12, the different frequencies (RF INPUT) are RF.

Regarding claims 13, 14, 17-20 and 22, the limitations recited in the claims are intended use of the invention.

Regarding claims 15 and 16, furthermore, it is well known to integrate the power amplifier transistor and first circuitry in order to form of small sized Integrated Circuit (IC). Therefore, it would have been obvious to have integrated the power amplifier transistor and first circuitry of Samay et al (Fig. 1) because such a modification would have advantageously produced a small-sized integrated circuit.

### ***Conclusion***

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Patent numbers (6,236,274; 6,737,923) are the amplifiers with the resonant circuits.

